

Vodavi Xtsc Ip Installation Manual

Processor and System-on-Chip Simulation ESL Models and their
Application Rainer Leupers Brian Bailey
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simulation of computer architectures has made rapid progress recently the primary application areas are hardware software performance estimation and optimization as well as functional and timing verification recent innovative technologies such as retargetable simulator generation dynamic binary translation or sampling simulation have enabled widespread use of processor and system on chip soc simulation tools in the semiconductor and embedded system industries simultaneously processor and soc simulation is still a very active research area e g what amounts to higher simulation speed flexibility and accuracy speed trade offs this book presents and discusses the principle technologies and state of the art in high level hardware architecture simulation both at the processor and the system on chip level

this book arises from experience the authors have gained from years of work as industry practitioners in the field of electronic system level design esl at the heart of all things related to electronic design automation eda the core issue is one of models what are the models used for what should the models contain and how should they be written and distributed issues such as interoperability and tool transportability become central factors that may decide which ones are successful and those that cannot get sufficient traction in the industry to survive through a set of real examples taken from recent industry experience this book will distill the state of the art in terms of system level design models and provide practical guidance to readers that can be put into use this book is an invaluable tool that will aid readers in their own designs reduce risk in development projects expand the scope of design projects and improve developmental processes and project planning

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